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IEC 61508 Hardware Safety Integrity Assessment

Grupo Moto Mecánica, Printed Circuit Board (PCB) L03-60590-01

Manufacturer:	Moto Mecánica Argentina S.A. Río Pilcomayo 449-499, B1657 El Libertador, Provincia de Buenos Aires, Argentina
Product:	Printed Circuit Board
Model:	L03-60590-01

Application:	Low Demand Mode Safety Instrumented Function
Safety Function:	A 4-20 mA analogue input signal (4-20 mA two-wire interface) is received by microcontroller U5 (LPC1769FBD100) which digitalizes the signal and compares the value to reference values stored in Serial Peripheral Interface (SPI) flash external memory. In case input signal value is outside of the safe range, a digital output signal is activated with a logic "0" value (Sensor and final element subsystems not part of this analysis)
Applied standard:	IEC 61508 Ed2 2010 Parts 1-3 (Partial)
Systematic Capability:	Not Assessed
Applicable report:	Environmental Resources Management Ltd Report: J038_FM001 Rev. 3

Assessment Route:	1_H
Hardware Fault Tolerance:	0
Type:	A/B (Refer to Table below)
Application restrictions:	None identified

Module	λDU (/hr)	λDD (/hr)	λS (/hr)	SFF	Type	SIL AC (HFT = 0)
Power Supply / Analogue Input / Digital Output	4.4E-07	3.8E-08	2.3E-06	84%	A	SIL 2
Microcontroller / Flash Memory	1.5E-08	5.8E-08	8.3E-08	90%	B	SIL 3

SIL AC (Architectural Constraints): Represents the maximum allowable SIL of each hardware element, assuming Hardware Fault Tolerance (HFT) = 0.

It should be noted that for a device to claim compliance with a specified SIL, the SIL AC and SC of the device needs to both be met. In addition to this, the required Average Probability of Failure

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on Demand (PFDavg) value for the complete Safety Instrumented Function (SIF) needs to adhere to the SIL PFDavg ranges.

IMPORTANT: It should be noted that this assessment considers only the Grupo Moto Mecánica, Printed Circuit Board (PCB) L03-60590-01. It does not cover opening and closing time requirements and a random hardware failures assessment must be conducted for the entire safety function for each application as per the appropriate Safety Requirements Specification.

The Grupo Moto Mecánica, Printed Circuit Board (PCB) L03-60590-01 has been assessed and is considered capable for use in a low demand mode safety function up to (and including) SIL 2, with regards to random hardware failures and architectural constraints.

Note 1: The SIL of a complete SIF (sensor, logic solver and final element subsystems) must be verified to calculate the required PFD / Probability of Failure per Hour (PFH), considering any redundancy, Proof Test Interval (PTI), Proof Test Coverage (PTC), Mission Time and Mean Time To Restoration (MTTR) for all elements included in the SIF. Each subsystem should be verified to ensure compliance with the minimum HFT requirements.

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